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November 7, 2007

Our Docket No. 789 127

To:

Examiner Sarvesh J. Nadkarni

From:

Stephen P. Burr

Re:

In re the Application of: Yukihisa TAKEUCHI, Tsutomu NANATAKI, Iwao OHWADA and

Takayoshi AKAO

Serial No.: 10/810,078

Art Unit:

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Date Filed: March 26, 2004

Conf. No.

3510

Title: CIRCUIT ELEMENT, SIGNAL PROCESSING CIRCUIT, CONTROL DEVICE, DISPLAY DEVICE, METHOD OF DRIVING DISPLAY DEVICE, METHOD OF DRIVING CIRCUIT

ELEMENT, AND METHOD OF DRIVING CONTROL DEVICE

I hereby certify that the following page(s) including this cover is/are being transmitted via EFS to the Patent and Trademark Office on November 7, 2007:

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